IN THE SPECIFICATION

Please amend paragraph [0030] in the manner shown below.

[0030]   To conserve power and offload processing requirements of the modem computing subsystem processor **204**, a DMA channel is provided to transfer data without processor intervention. A DMA Controller/microprocessor memory interface **260** is provided to allow direct memory access of the shared memory modules **108-110** by the modem subsystem processor **204** and the modem peripherals **244** via a memory interface bridge **262**.